

**Tuesday, 10 March**

9:30 – 13:00	Class 2.1 Faster. Smarter. Firmware. Modern Best Practices for Better Embedded Systems	Class 2.2 Hands-On Zephyr Project Workshop	Class 4.1 Soft Logic, Hard Math: Building a RISC-V FPU With IEEE 754 Support on FPGAs	Class 7.1 Introduction to tinyML – Deploying Deep Learning Models Onto Low-power Micro-Controllers
14:00 – 17:00	Class 4.2 Safeguarding Industrial Interfaces: Reliable Protection Against Transient Overvoltage	Class 5.1 Rust, a Safe Language for Low-level Programming	Class 5.2 GitLab for Embedded DevOps: Integrated AI for Both DevSecOps Adoption and Product Delivery	Class 7.2 Edge AI: Evolution and Hands-on

**Wednesday, 11 March**

Class 3.1 CRA Regulations and Certification	Class 5.3 Programming With Rust for C/C++ Programmers	Class 5.4 C++ and Modern C++ for Embedded Development	Class 7.3 From Vision to Deployment: Developing Secure AI-Enabled Linux Devices
Class 2.3 Embedded GNU/Linux in Mid-integrity/ Mixed-criticality Safety-related Systems	Class 2.4 Embedded Linux Security Exercised on the Secure Platform GyroidOS	Class 3.2 Cyber Resilience Act (CRA) – Practical Implementation Examples	Class 3.3 Embedded Safety Architectures

**Thursday, 12 March**

Class 2.5 Introduction to Embedded Linux Using a Yocto Project SDK	Class 4.3 FPGA-Design Using C/C++ and High-Level Synthesis	Class 4.4 Designing Battery-free IoT	Class 5.5 Embedded Software Testing – With Fundamental Skills and Artificial Intelligence
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1. IOT & CONNECTIVITY		2. EMBEDDED OS	3. SAFETY & SECURITY		4. HARDWARE DESIGN		5. SOFTWARE & SYSTEMS ENGINEERING		6. EMBEDDED VISION	7. EDGE AI	8. USE CASES FOR EMBEDDED
Session 1.1 Ambient IoT (powered by Ambient IoT Alliance)	Session 1.4 WiFi and Long-Range	Session 2.1 Long-Term Stability with Yocto (powered by Yocto Project)	Session 3.1 Implementing the Cyber Resilience Act (CRA)	Session 3.4 Open Source for Safety & Security 1	Session 4.1 Chiplets in Automotive Applications (powered by UCIe)		Session 5.1 Programming Languages: Rust	Session 5.4 Software Architectures (powered by Eclipse Foundation)	Session 6.1 Open Source Solutions		
Session 1.2 IoT Ecosystem Technologies 1	Session 1.5 CAN Technologies (powered by CiA)	Session 2.2 Yocto Use Cases (powered by Yocto Project)	Session 3.2 Post Quantum Cryptography Strategies	Session 3.5 Open Source for Safety & Security 2	Session 4.2 Chiplets - Certification, Validation & Test (powered by UCIe)		Session 5.2 Programming Languages	Session 5.5 Development Processes for Software Defined Vehicles (SDV)	Session 6.2 Hardware Acceleration		
Session 1.3 IoT Ecosystem Technologies 2	Session 1.6 CAN Safety & Security (powered by CiA)	Session 2.3 RTOS Orchestration	Session 3.3 Long-Term & Post Quantum Security	Session 3.6 Trusted Artificial Intelligence	Session 4.3 MIPI Interfaces (powered by MIPI Alliance)		Session 5.3 MISRA SW-Coding Guidelines (powered by MISRA)	Session 5.6 Open Source Software	Session 6.3 Pipelines (powered by Edge AI & Vision Alliance)		
Session 1.7 Ethernet Time Sensitive Networking (TSN)	Session 1.10 Cellular - Emerging Technologies	Session 2.4 Zephyr - Best Practice (powered by Zephyr Project)	Session 3.7 Reliable Architectures		Session 4.4 System on Chip Design Process		Session 5.7 Testing Embedded Software		Session 6.4 Emerging Embedded Vision and Audio Technologies	Session 7.1 Lightweight Embedded AI	Session 8.1 Medical Applications
Session 1.8 Single-Pair Ethernet (powered by SPE System Alliance)	Session 1.11 Cellular - 5G	Session 2.5 Zephyr in Safety-Critical Applications (powered by Zephyr Project)	Session 3.8 Reliable Code / Safe Rust		Session 4.5 System Hardware - Design Examples		Session 5.8 Trustable Embedded Software		Session 6.5 EV Use Cases	Session 7.2 Neuromorphic Computing	Session 8.2 Predictive Maintenance Applications
Session 1.9 Applying Ethernet Technologies	Session 1.12 Cellular - eSIM	Session 2.6 Zephyr Use Cases (powered by Zephyr Project)	Session 3.9 Reliability Testing		Session 4.6 Architectural Design		Session 5.9 Formal Verification		Session 6.6 Radar and Artificial Intelligence	Session 7.3 Qualification and Validation	Session 8.3 Mobility Applications
Session 1.13 Bluetooth - Best Practices (powered by Bluetooth SIG)		Session 2.7 Container Use Cases	Session 3.10 Ensuring Resilient Embedded Systems		Session 4.7 Hardware Design and Production Process (powered by FED)	Session 4.10 RISC-V Ecosystem (powered by RISC-V Foundation)	Session 5.10 DevOps & CI/CD Pipeline	Session 5.13 Digital Twin		Session 7.4 Execution	Session 8.4 Development Processes
Session 1.14 Bluetooth - Emerging Technologies (powered by Bluetooth SIG)		Session 2.8 Beyond Virtualization	Session 3.11 Trustworthy Systems with CHERI & RISC-V (powered by CHERI Alliance)		Session 4.8 Hardware Design Examples	Session 4.11 RISC-V Applications (powered by RISC-V Foundation)	Session 5.11 Workflows and Tools	Session 5.14 Software Debugging and Tracing		Session 7.5 From Lab to Field	Session 8.5 Security Applications 1
Session 1.15 Bluetooth in Automotive Applications (powered by Bluetooth SIG)		Session 2.9 Real-Time Virtualization	Session 3.12 DevSecOps for Safe & Secure Systems		Session 4.9 Power Supply Design	Session 4.12 Open-Source SoC Hardware	Session 5.12 Technical Debt and Legacy	Session 5.15 Generating Flexible & Efficient Code		Session 7.8 Use Cases (powered by EDGE AI FOUNDATION)	Session 8.6 Security Applications 2